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Bib Data Sheet

CONFIRMATION NO. 3078

<b>SERIAL NUMBER</b> 10/517,943	<b>FILING OR 371(c) DATE</b> 12/13/2004 <b>RULE</b>	<b>CLASS</b> 338	<b>GROUP ART UNIT</b> 2832	<b>ATTORNEY DOCKET NO.</b> 10921.262USWO
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## APPLICANTS

Torayuki Tsukada, Kyoto, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP03/07456 06/12/2003

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

## ADDRESS

23552

## TITLE

Chip resistor having low resistance and its producing method

<b>FILING FEE RECEIVED</b> 1600	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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